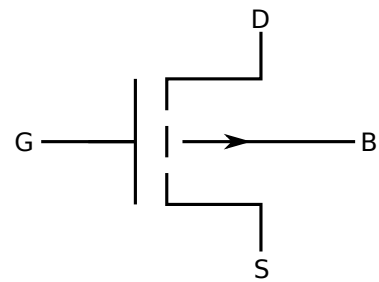
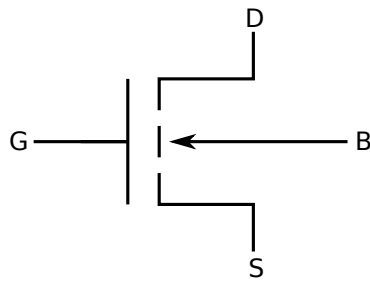


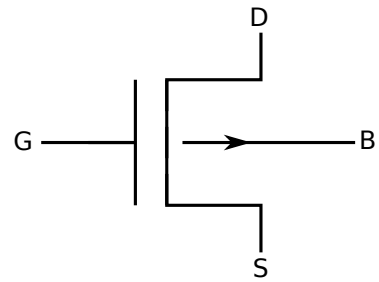
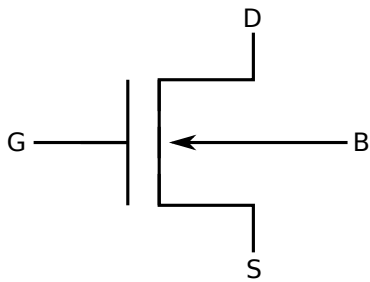
# NMOS

# PMOS

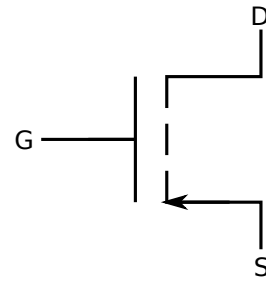
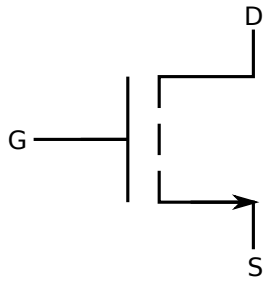
Enhancement Mode  
4-terminal



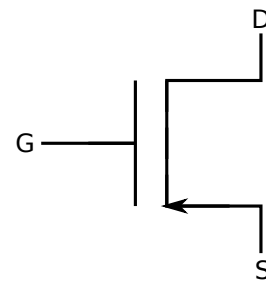
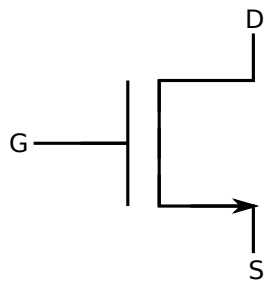
Depletion Mode  
4-terminal



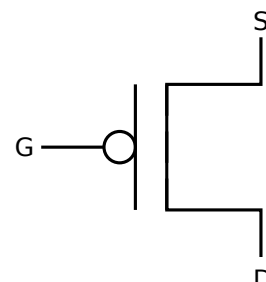
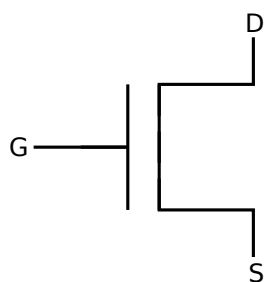
Enhancement Mode  
3-terminal



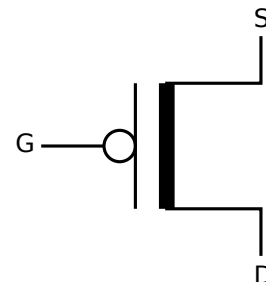
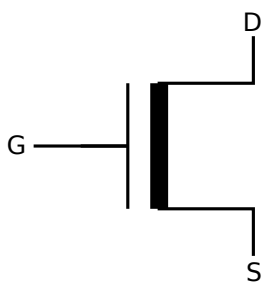
Depletion Mode  
3-terminal



Enhancement Mode  
3-terminal Shorthand



Depletion Mode  
3-terminal Shorthand



PMOS Transistors nearly always have their source terminals tied to the more positive supply, so in shorthand it is assumed this is the case.